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Application No.: 10/669,371
Art Unit 2871

JUL 09 2008 Attorney Docket No. 0465-1880PUS1
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Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend claims as follows.

Listing of Claims:

1. (Currently Amended) A line-on glass liquid crystal display panel, comprising:
a picture display part ~~with a matrix of~~ having liquid crystal cells at each intersection of gate lines and data lines;
data pads extended from the data lines in an outer area of the picture display part;
gate pads extended from the gate lines in the outer area of the picture display part;
a first and a second line-on glass signal pads near to an end of the data pads and an end of the gate pads, respectively, in one corner of the outer area of the picture display part;
a plurality of line-on glass type signal lines connecting the first and second line-on glass signal pads in the corner of the outer area of the picture display part ~~located in an outer area of the picture display part for applying gate power voltage signals and gate control signals to gate drive ICs in order to drive signals~~ gate signal lines of the picture display part to drive the liquid crystal cells; and
at least onea dummy line formed between the line-on glass type signal lines for applying a common voltage as a reference voltage to drive the liquid crystal cells with at least onea layer of insulating film therebetween, wherein the insulating film covers the plurality of line-on glass type signal lines and the dummy line is formed on the layer of the insulating film.
2. (Currently Amended) The line-on glass liquid crystal display panel according to claim 1, ~~further comprising:~~
wherein the first and a-second line-on glass signal pads which extend from both sides of the line-on glass type signal lines and are respectively formed between the end of the gate pads and the end of the data pads.

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3. (Currently Amended) The line-on glass liquid crystal display panel according to claim 2, further comprising:

first and a second dummy pads that extend from both sides of the dummy line between the gate pads and the data pads.

4. (Original) The line-on glass liquid crystal display panel according to claim 3, wherein the first and second dummy pads are located between the first and second line-on glass type signal pads.

5. (Currently Amended) The line-on glass liquid crystal display panel according to claim 1, wherein the line-on glass type signal lines are formed in a same layer as the gate line of the picture display part.

6. (Currently Amended) The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as the data line of the picture display part ~~that crosses the gate line with a gate insulating film therebetween~~.

7. (Withdrawn) The line-on glass liquid crystal display panel according to claim 6, wherein the dummy line is located between the line-on glass type signal lines with the gate insulating film therebetween.

8. (Withdrawn) The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as a pixel electrode of the picture display part.

9. (Withdrawn) The line-on glass liquid crystal display panel according to claim 8, wherein the dummy line is located between the line-on glass type signal lines with a gate insulating film and a protective film therebetween, and the gate insulating film and the protective

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film are formed to cover the gate line.

10-11 (Canceled)

12. (Currently Amended) A fabricating method of a line-on glass liquid crystal display panel, comprising:

forming gate lines in a picture display part and a plurality of line-on glass signal lines in one corner of an outer area of the picture display part on a substrate for applying gate power voltage signals and gate control signals to gate drive ICs in order to drive gate signal lines of the picture display part;

forming at least one layer of insulating film to cover the line-on glass type signal lines;
and

forming data lines to cross the gate lines in a picture display part and a dummy line that is located between the line-on glass signal lines on the insulating film for applying a common voltage as a reference voltage; and

forming data pads extended from the data lines and gate pads extended from the gate lines in the outer area of the picture display part and forming first and second line-on glass signal pads near to the data pads and gate pads, respectively, in one corner of the outer area of the picture display part.

wherein the first and the second line-on glass signal pads are connecting to the plurality of the line-on glass signal lines.

13. (Currently Amended) The fabricating method according to claim 12, further comprising:

~~forming a gate line of the picture display part on a substrate and a gate electrode connected to the gate line of the picture display part on the substrate;~~

forming a gate insulating film on the substrate on which the gate line and the gate electrode are formed;

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forming a semiconductor layer on the gate insulating film;
forming a ~~data line crossing the gate line~~, a source electrode connected to the data line, and a drain electrode opposite to the source electrode with a designated gap therebetween, on the substrate on which the semiconductor is formed;
forming a protective film on the substrate where the data line, the source electrode and the drain electrode are formed; and
forming a pixel electrode connected to the drain electrode on the protective film.

14. (Currently Amended) The fabricating method according to claim ~~13~~12, wherein the line-on glass signal line is formed of a same metal as ~~the~~ gate line.

15. (Original) The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as the data line.

16. (Original) The fabricating method according to claim 15, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film therebetween.

17. (Withdrawn-Currently Amended) The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as ~~the~~ pixel electrode.

18. (Withdrawn) The fabricating method according to claim 17, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film and the protective film therebetween.

19. (Currently Amended) A line-on glass liquid crystal display panel, comprising:
a picture display part with a matrix of liquid crystal cells having a plurality of gate lines and data lines to cross each other;
a gate pad and a data pad to drive the gate lines and data lines, respectively;

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a plurality of line-on glass type signal lines located in one corner of an outer area of the picture display part of a lower substrate for applying drive signals to drive the liquid crystal cells, wherein the one corner of the outer area of the picture display part is corresponding to between the gate pad and the data pad; and

a plurality of common voltage signal lines for applying a common voltage signal and being formed between gate signal lines,

wherein at least one of the plurality of common voltage lines applies the common voltage signal through a silver(Ag) dot to a common electrode that is formed on an entire surface of an upper substrate.

20. (Previously Presented) The line-on glass liquid crystal display panel according to claim 19, wherein the gate signal lines are Vgl, Vcc, Vgh, GOE, GSC, GSP.

21. (Currently Amended) A line-on glass liquid crystal display panel, comprising:
a picture display part with a matrix of liquid crystal cells having a plurality of gate lines and data lines to cross each other;

a gate pad and a data pad to drive the gate lines and data lines, respectively;

a plurality of line-on glass type signal lines located in one corner of an outer area of the picture display part of a lower substrate for applying drive signals to drive the liquid crystal cells;
and

a plurality of dummy lines that formed between gate signal lines,
wherein the plurality of dummy lines are a common voltage line or a ground voltage line.

22. (Previously Presented) The line-on glass liquid crystal display panel according to claim 21, wherein the gate signal lines are Vgl, Vcc, Vgh, GOE, GSC, GSP.

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23. (Previously Presented) The line-on glass liquid crystal display panel according to claim 21, wherein the common voltage line applies the common voltage signal through a silver(Ag) dot to a common electrode that is formed on an entire surface of an upper substrate.